



SOSPB-4599-80 SFP+ bidi Tx1490nm/Rx1550nm 80km  
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**RoHS Compliant 10Gb/s SFP+ 80km CWDM BiDi Optical Transceiver**



## PRODUCT FEATURES

- λ SFP MSA package with single LC connector
- λ 1470nm~1610nm transmitter, minimum interval 20nm
- λ APD receiver for high sensitivity
- λ Very low EMI and excellent ESD protection
- λ Digital Diagnostic Monitor Interface
- λ Hot pluggable
- λ Support 1.25G~10Gb/s serial optical interface
- λ Up to 80km distance
- λ Compliant with SFP+ MSA
- λ High transmission margin
- λ +3.3V single power supply
- λ Below <1.2W power consumption

## APPLICATIONS

- λ 10GBASE-BX at 10.3125 Gb/s
- λ Other optical link

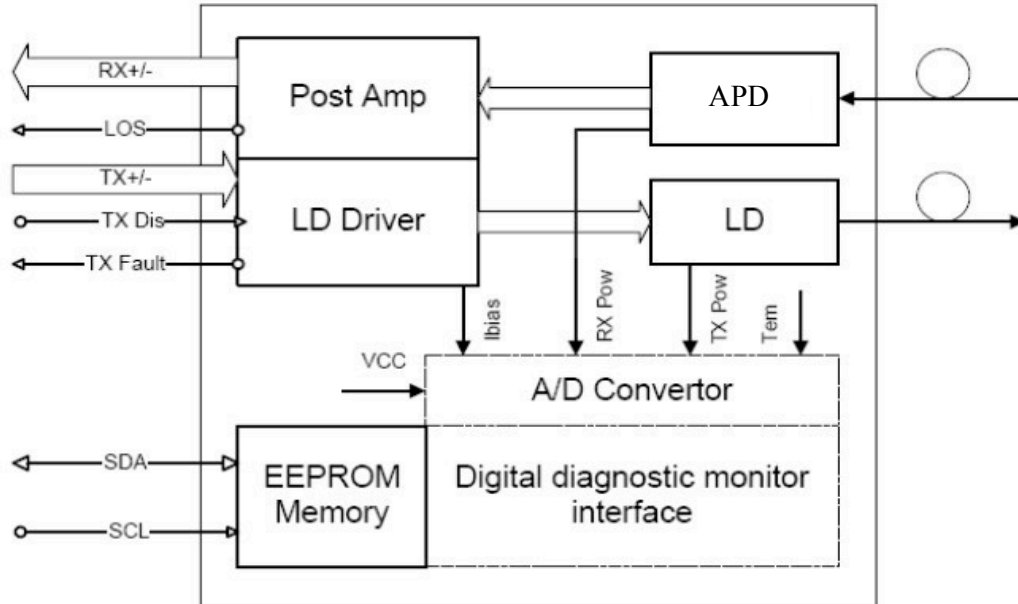
## STANDARD

- λ SFP+ MSA Compliant
- λ SFF-8472 reversion 9.5 compliant
- λ IEEE802.3-2005 compliant
- λ Telcordia GR-468-CORE compliant
- λ FCC 47 CFR Part 15,Class B compliant
- λ FDA 21 CFR 1040.10 and 1040.11,class1 compliant
- λ RoHS compliant

## PRODUCT DESCRIPTIONS

SINOVO'S SOSPB-4596-80 SOSPB-5496-80 is hot pluggable 3.3V Small-Form-Factor transceiver module. It designed expressly for high-speed communication applications that require rates up to 10.7Gbps,it designed to be compliant with SFF-8472 and SFP+ MSA. The module data link up to 70km in 9/125um single mode fiber. The optical output can be disabled by a LVTTTL logic high-level input of Tx Disable. Tx Fault is provided to indicate that degradation of the laser. Loss of signal (LOS) output is provided to indicate the loss of an input optical signal of receiver or the link status with partner.

## FUNCTIONAL DIAGRAM



## ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min.	Max.	Unit	Note
Supply Voltage	V <sub>cc</sub>	-0.5	4.0	V	
Storage Temperature		-40	85	°C	
Relative Humidity			85	%	

Note: Stress in excess of the maximum absolute ratings can cause permanent damage to the module

## GENERAL OPERATING CHARACTERISTICS

Parameter	Symbol	Min.	Typ	Max.	Unit	Note
Data Rate	Ethernet		10.3125		Gb/s	
	Fiber Channel		9.953			
Supply Voltage	V <sub>cc</sub>	3.13	3.3	3.47	V	
Supply Current	I <sub>ccs</sub>			350	mA	
Operating Case Temp.	T <sub>c</sub>	0		70	°C	

## ELECTRICAL INPUT/OUTPUT CHARACTERISTICS

### λ Transmitter

Parameter	Symbol	Min.	Typ	Max.	Unit	Note
Diff. input voltage swing		120		820	mVpp	1
Tx Disable input	H	VIH	2.0	Vcc+0.3	V	
	L	VIL	0	0.8		
Tx Fault output	H	VOH	2.0	Vcc+0.3	V	2
	L	VOL	0	0.8		
Input Diff. Impedance	Zin		100		Ω	

### λ Receiver

Parameter	Symbol	Min.	Typ	Max.	Unit	Note
Diff. output voltage swing		340	650	800	mVpp	3
Rx LOS Output	H	VOH	2.0	Vcc+0.3	V	2
	L	VOL	0	0.8		

Note 1) TD+/- are internally AC coupled with 100Ω differential termination inside the module.

Note 2) Tx Fault and Rx LOS are open collector outputs, which should be pulled up with 4.7k to 10kΩ resistors on the host board. Pull up voltage between 2.0V and Vcc+0.3V.

Note 3) RD+/- outputs are internally AC coupled, and should be terminated with 100Ω (differential) at the user SERDES.

## OPTICAL CHARACTERISTICS

### λ Transmitter (0~70 @10.3125Gb/s)

Parameter	Symbol	Min.	Typ	Max.	Unit	Note
Operating Wavelength	OLSC451XL-	1483	1490	1497	nm	1
	OLSC541XL-	1543	1550	1557		
Ave. output power (Enabled)	Po	+1		+4	dBm	2
Extinction Ratio	ER	6			dB	2
RMS spectral width	ΔΔ			1	nm	
Rise/Fall time (20%~80%)	Tr/Tf			50	ps	3
Optical modulation amplitude	OMA	-4.8			dBm	
Dispersion penalty				3	dB	
Output Optical Eye		IEEE 802.3-2005 Compliant				

### λ Receiver (0~70 @10.3125Gb/s)

Parameter	Symbol	Min.	Typ	Max.	Unit	Note
Operating Wavelength	OLSC451XL-	1543	1550	1557	nm	1
	OLSC541XL-	1483	1490	1497		
Sensitivity	Psen			-20	dBm	4
Min. overload	Pimax	-7			dBm	
LOS Assert	Pa	-40			dBm	
LOS De-assert	Pd			-22	dBm	
LOS Hysteresis	Pd-Pa	0.5		4	dB	

Note 1) typical wavelegths 1490nm and 1550nm, 1470nm~1610nm transmitter, minimum interval 20nm.

Note 2) Measured at 10.3125b/s with PRBS 2<sup>n</sup> - 1 NRZ test pattern.

Note 3) 20%~80%

Note 4) Under the ER worst case, measured at 10.3125 Gb/s with PRBS 2<sup>31</sup> - 1 NRZ test pattern for BER < 1x10<sup>-12</sup>

## SERIAL INTERFACE FOR ID AND DDM

### ● Serial Interface for ID and DDM

The SFP modules implement the 2-wire serial communication protocol as defined in the SFP MSA.

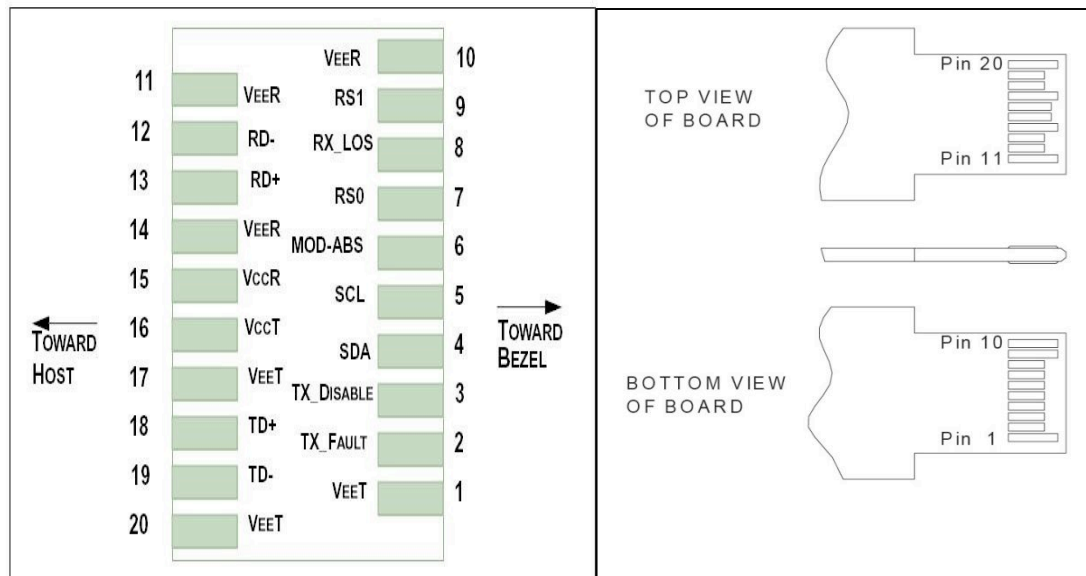
The serial ID information of the SFP modules and Digital Diagnostic Monitor parameters can be accessed through the I2C interface at address A0h and A2h. The memory is mapped in Table 1. Detailed ID information(A0h) And the DDM specification(A2h). For more details of the memory map and byte definitions, please refer to the SFF-8472 (Rev 9.3, Aug. 2002), "Digital Diagnostic Monitoring Interface for Optical Transceivers".

The DDM parameters have been internally calibrated.

Table 1. Digital Diagnostic Memory Map (Specific Data Field Descriptions)

2 wire address 1010000X (A0h)		2 wire address 1010001X (A2h)	
Address	Information	Address	Information
0~95	Serial ID Defined by SFP MSA (96 bytes)	0~55	Alarm and Warning Thresholds (56 bytes)
96~127	Vendor Specific (32 bytes)	56~95	Calibration Constants (40 bytes)
128~255	Reserved, SFF8079 (128 bytes)	96~119	Real Time Diagnostic Interface (24 bytes)
		120~127	Vendor Specific (8 bytes)
		128~247	User Writable EEPROM (120 bytes)
		248~255	Vendor Specific (8 bytes)

## PIN DEFINITIONS AND FUNCTIO



PIN #	Name	Function	Notes
1	VeeT	Module Transmitter Ground	Note 1
2	Tx Fault	Module Transmitter Fault	Note 2
3	Tx Disable	Transmitter Disable; Turns off transmitter laser output	Note 3
4	SDL	2 wire serial interface data input/output(SDA)	
5	SCL	2wire Serial interface clock input (SCL)	
6	MOD-ABS	Module Absent, connect to VeeR or Vee T in the module	Note 2
7	RS0 $\Omega$	Rate select0, optionally control SFP+ receiver. When high, input data rate >4.5Gb/s; when low, input data rate <=4.5Gb/s	
8	LOS	Receiver Loss of Signal Incication	Note 1
9	RS1	Rate select0, optionally control SFP+ transmitter When high, input data rate >4.5Gb/s; When low, input data rate <=4.5Gb/s	Note 1
10	VeeR	Module Reveiver ground	
11	VeeR	Module receiver ground	
12	RD-	Receiver inverted data out put	
13	RD+	Receiver non-inverted data out put	Note 1
14	VeeR	Module receiver ground	
15	VccR	Module receiver 3.3V supply	
16	VccT	Module transmitter	

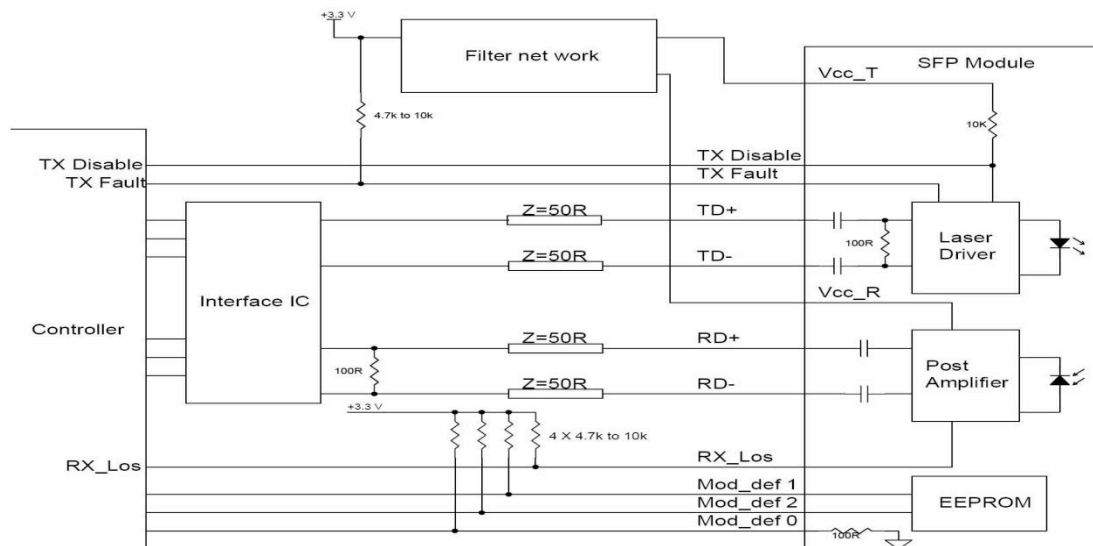
17	VeeT	3.3V supply Module transmitter ground	Note 1
18	TD+	Transmitter inverted data out put	
19	TD-	Transmitter non-inverted data out put	

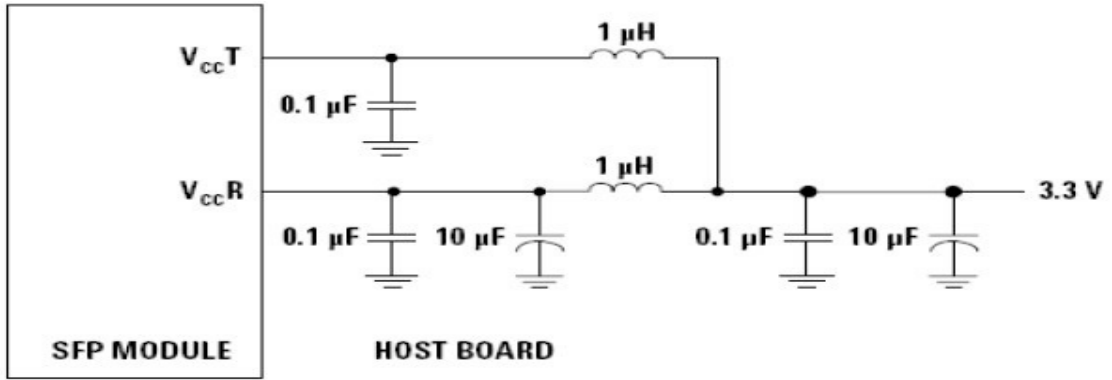
Note 2) This pin is an open collector/drain output pin and shall be pulled up with 4.7K-10Kohms to Host\_Vcc on the host board.

Note 3) This pin shall be pulled up with 4.7K-10Kohms to VccT in the module.

Note 4) This pin is an open collector/drain output pin and shall be pulled up with 4.7K-10Kohms to Host\_Vcc on the host board. In FC designated as RX\_LOS, inSONET designated as LOS, and in Ethernet designated at Signal Detect.

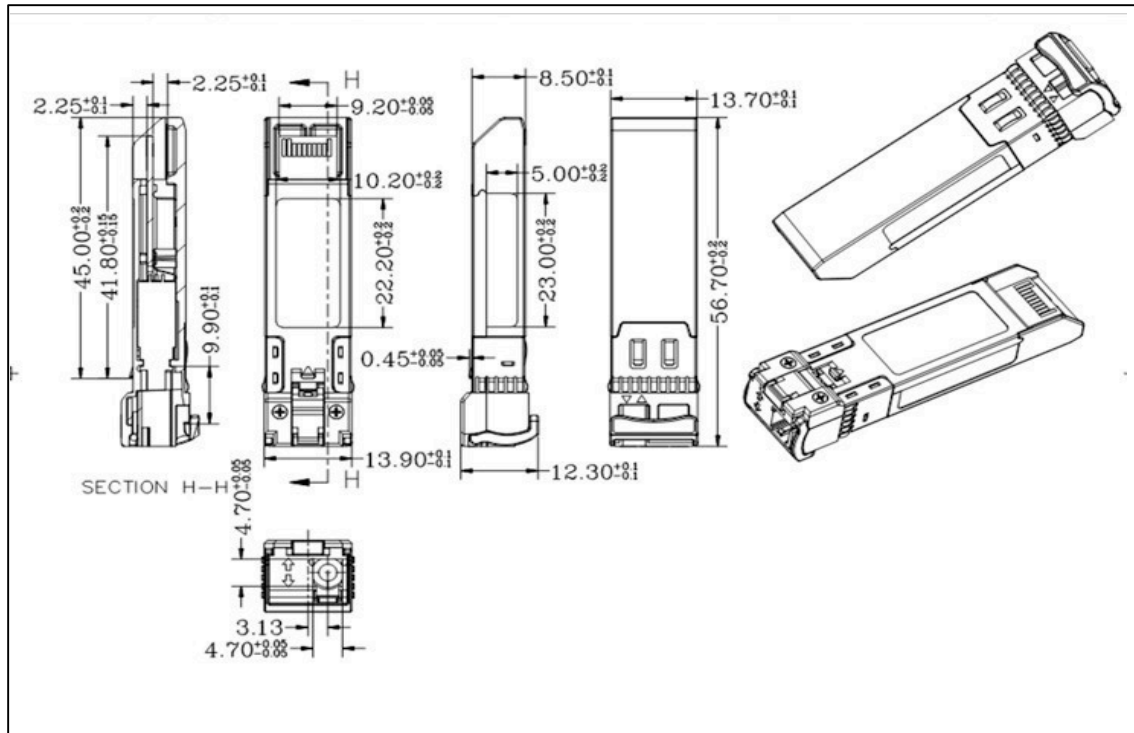
### TYPICAL INTERFACE CIRCUIT





Note: Inductors with DC resistance of less than  $1\ \Omega$  should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30 mA greater than the steady state value

## PACKAGE DIMENSIONS





## ORDERING INFORMATION

<b>Part Number</b>	<b>Description</b>
SOSPb-4599-80	SFP Plus BiDi,CWDM,10.3125Gbps, 80KM,0~70℃, with DDM
Xx means:	CWDM wavelength, 45=1490/1550nm
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